

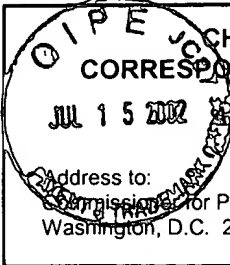
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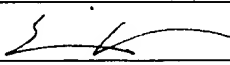
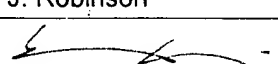
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	Filing Date	April 5, 2001
	First Named Inventor	Tatsuya ARAO et al.
	Group Art Unit	2811
	Examiner Name	T. Tran
	Attorney Docket Number	0756-2293

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Bib Data Sheet

CONFIRMATION NO. 5035

SERIAL NUMBER 09/826,416	FILING DATE 04/05/2001 RULE	CLASS 257	GROUP ART UNIT 2811	ATTORNEY DOCKET NO. 0756-2293	
APPLICANTS Tatsuya Arao, Atsugi-shi, JAPAN; Hideomi Suzawa, Atsugi-shi, JAPAN;					
** CONTINUING DATA ***** NONE TI					
** FOREIGN APPLICATIONS ***** JAPAN 2000-140592 05/12/2000 YES TI					
IF REQUIRED, FOREIGN FILING LICENSE GRANTED ** 05/18/2001					
Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance Verified and Acknowledged <u>Examiner's Signature</u> Initials		STATE OR COUNTRY JAPAN	SHEETS DRAWING 9	TOTAL CLAIMS 29	INDEPENDENT CLAIMS 6
ADDRESS 31780					
TITLE Semiconductor device and manufacturing method therefor					
FILING FEE RECEIVED 1594	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:		<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit		